

Substitute for forms 1449A/PTO & 1449B/PTO	ATTORNEY'S DKT NO. 009683-475	APPLICATION NO. Unassigned
FIRST INFORMATION DISCLOSURE STATEMENT BY APPLICANT	APPLICANT Hiroshi SHIROTA	
	FILING DATE July 11, 2003	GROUP Unassigned

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

SEARCHED DOCUMENTS					
Examiner Initials	Document Number	Kind Code (if known)	Country	Date of Publication (MM-DD-YYYY)	Translation Yes No
	*6-311208		Japan	11-04-1994	Abst
	2001-34949		Japan	02-09-2001	Abst

NON-PATENT LITERATURE DOCUMENTS

Examiner Initials	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
/AF/	*Jon LUEKER et al., "USB 2.0 Transceiver Macrocell Interface (UTMI) Specification Version 1.05," March 29, 2001, pp. 1-67, Intel Corporation.
/AF/	Hiroshi SHIROTA et al., "Data/Clock Recovery Circuit for Recovering Data and Clock Signal with High Accuracy," Application Serial No. 10/193,064, filed July 12, 2002, Attorney's Docket No. 027260-574.
/AF/	Katsuya MIZUMOTO et al., "Data Transfer Device for Transferring Data Between Blocks of Different Clock Domains," Application Serial No. 10/352,164, filed January 28, 2003, Attorney's Docket No. 027260-644.

Examiner Signature	/Aristocratis Fotakis/	Date Considered	09/20/2007
-----------------------	------------------------	--------------------	------------

EXAMINER: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. **SEND TO:** Assistant Commissioner for Patents, Washington, DC 20231.

*Also discussed in the specification.